

IFW



ATTENTION:

**OFFICE OF INITIAL
PATENT EXAMINATION'S
FILING RECEIPT CORRECTIONS**

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant(s): Toshiro AKINO
Application No.: 10/551,266
Conf. No.: 3385
Group No.: 2816
Filed: September 29, 2005
For: LATERAL BIPOLAR CMOS INTEGRATED CIRCUIT
Docket No.: 9694D-000025/US

LETTER REQUESTING CORRECTED OFFICIAL FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 9, 2007

Sir:

The Official Filing Receipt mailed January 25, 2007 (copy attached herewith) does not reflect the correct spelling of the assignee in the assignment for published patent application portion of the subject application. The correct spelling should be listed as:

Juridical Foundation Osaka Industrial Promotion ~~Org~~, Osaka, Japan

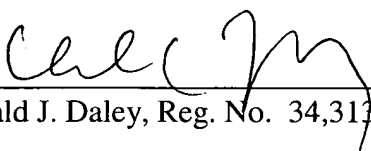
Juridical Foundation Osaka Industrial Promotion Organization, Osaka, Japan

Applicant respectfully requests issuance of a corrected Official Filing Receipt. Changes to be made are indicated in red on the attached copy of the Official Filing Receipt.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment of Deposit Account No. 08-0750 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

HARNESS, DICKEY & PIERCE, P.L.C

By 
Donald J. Daley, Reg. No. 34,313

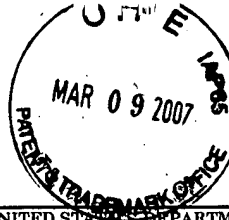
P.O. Box 8910
Reston, VA 20195
(703) 668-8000

DJD/jap

Attachment: Copy of Official Filing Receipt with requested corrections marked in red ink.



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO.	DRAWINGS	TOT CLMS	IND CLMS
10/551,266	09/29/2005	2816	900	9694D-000025/US	35	8	1

CONFIRMATION NO. 3385

30593

HARNESS, DICKEY & PIERCE, P.L.C.

P.O. BOX 8910

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FILING RECEIPT



OC000000022110098

Date Mailed: 01/25/2007

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Toshiro Akino, Wakayama, JAPAN;

Assignment For Published Patent Application

JURIDICAL FOUNDATION OSAKA INDUSTRIAL PROMOTION-OR, Osaka, JAPAN

ORGANIZATIONPower of Attorney: The patent practitioners associated with Customer Number 30593.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/03208 03/11/2004

Foreign Applications

JAPAN 2003-095116 03/31/2003

If Required, Foreign Filing License Granted: 01/23/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/551,266**

Projected Publication Date: 05/03/2007

Non-Publication Request: No

Early Publication Request: No

Title

Lateral bipolar cmos integrated circuit

Preliminary Class

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